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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/765,100	01/28/2004	Johngeon Shin	1630-0135P	9721
2292	7590 05/26/2005		EXAM	INER
BIRCH STEV PO BOX 747	WART KOLASCH &	TRAN, MAI HUONG C		
FALLS CHURCH, VA 22040-0747			ART UNIT	PAPER NUMBER

DATE MAILED: 05/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	10/765,100	SHIN, JOHNGEON			
Office Action Summary	Examiner	Art Unit			
	Mai-Huong Tran	2818 .			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period v - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tim y within the statutory minimum of thirty (30) days vill apply and will expire SIX (6) MONTHS from to a cause the application to become ABANDONED	ely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).			
Status					
Responsive to communication(s) filed on <u>29 April 2005</u> . This action is FINAL . 2b) This action is non-final. Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims					
 4) ☐ Claim(s) 1-19 is/are pending in the application. 4a) Of the above claim(s) 7-19 is/are withdrawn from consideration. 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-6 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or election requirement. 					
Application Papers					
 9) The specification is objected to by the Examine 10) The drawing(s) filed on 27 August 2004 is/are: Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex 	a) accepted or b) objected to drawing(s) be held in abeyance. See ion is required if the drawing(s) is objected to be accepted.	ected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary (Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:				

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DETAILED ACTION

Election Restriction

Applicant's election with traverse of Group I (claims 1-6) drawn to a semiconductor device is acknowledged. Accordingly, claims 7-19 are withdrawn from consideration as being directed to a non-elected invention. See 37 CFR 1.142(b) and MPEP § 821.03.

Because Applicant did not distinctly and specifically point out the supposed error in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)). Applicant has the right to file a divisional application covering the subject matter of the non-elected claims.

The traversal is on the ground(s) that see the election paper. This is not found persuasive because the fields of search for method' and device claims are NOT coextensive and the determinations of patentability of method and device claims are different, that is process limitations and device limitations are given weight differently in determining the patentablitity of the claimed inventions. Also, the strategies for doing text searching of the device claims and method claims are different. Thus, separate searches are required.

The requirement is still deemed proper and is therefore made FINAL.

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Drawings

The drawings are objected to for the following reasons.

Figure 1 is not designated by a legend such as "Prior Art". The Legend is necessary in order to clarify what applicant's invention is (see MPEP § 608.02g).

Applicant is required to submit a proposed drawing correction, showing changes in red ink, in response to this Office action. However, formal correction of the noted defect(s) can be deferred until the application is allowed by the examiner (see MPEP § 608.02v).

Specification

The specification is objected to for the following reasons.

Typographical error, "... cathode ray tube <u>fu</u> the third step ..." has been found on page 3, [0015]. Correction is required.

Claim Rejections - 35 U.S.C. § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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Claims 1-3 and 6 are rejected under 35 U. S. C. § 102 (b) as being anticipated by US Publication No. 2002/0125491 to Shibata et al.

Regarding to claim 1, Shibata discloses a nitride semiconductor comprising a metal oxide layer 1, a first nitride buffer layer 2 which includes metal, formed on the metal oxide layer 1, a second nitride buffer layer 3 formed on the first nitride buffer layer 2, and a nitride layer 4 formed on the nitride buffer layers 2, 3 (page 1, [0017], [0018], page 2, [0026], [0035], page 3, [0036], [0038], [0039], and fig. 1).

Regarding to claim 2, the nitride semiconductor wherein the metal oxide layer is a sapphire substrate and the metal is aluminum (page 2, [0026]).

Regarding to claim 3, the nitride semiconductor wherein the first and second nitride buffer layers include Indium (page 1, [0018], page 2, [0032]).

Regarding to claim 6, the nitride semiconductor wherein nitride film 2 is formed between the metal oxide layer 1 and first nitride buffer layer 3 (page 1, [0017], fig. 1).

Claim Rejections - 35 U.S.C. § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 4, 5 are rejected under 35 U.S.C. 103 (a) as being unpatentable over US Publication No. 2002/0125491 to Shibata et al. in view of Sone et al. (US Pub. No. 2002/0192373).

Regarding to claim 4, Shibata discloses the claimed invention except for the nitride semiconductor wherein the second nitride buffer layer is a bivalent nitride layer.

Sone teaches the second nitride buffer layer is a bivalent nitride layer (page 1, [0019], and fig. 2).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the second nitride buffer layer is a bivalent nitride layer, as taught by Sone in order to improve characteristics of the thin film by performing thin film growth in two steps (page 1, [0002]).

Regarding to claim 5, Sone teaches third nitride buffer layer 13 which does not include metal, is formed between the first nitride buffer layer 12 and the second nitride buffer 14 (page 1, [0019], page 2, [0020], and fig. 2).

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Conclusion

Any inquiry concerning this communication on earlier communications from the examiner should be directed to Mai-Huong Tran, (571) 272-1796. The examiner can normally be reached on Monday-Thursday from 8:00 AM to 6:30 PM. The examiner's supervisor, David Nelms can be reached on (571) 272-1787.

The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR, Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mai-Huong Tran